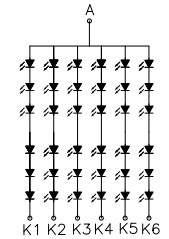


DETAIL A  
(SCALE 50:1)



BACKLIGHT CIRCUIT DIAGRAM

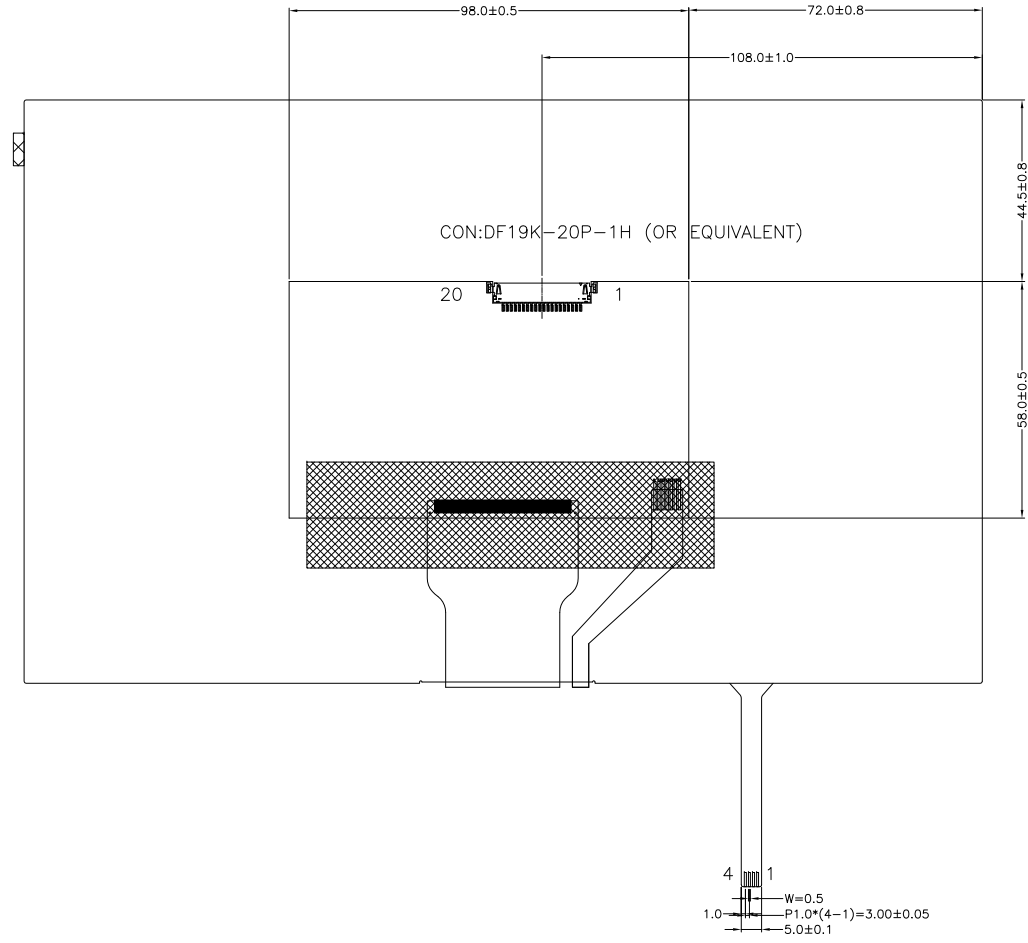
PIN ASSIGNMENT FOR LCM	
PIN NO.	SYMBOL
1	VCC
2	VCC
3	VSS
4	VSS
5	RIN0-
6	RIN0+
7	VSS
8	RIN1-
9	RIN1+
10	VSS
11	RIN2-
12	RIN2+
13	VSS
14	RCLK-
15	RCLK+
16	VSS
17	VDD_5V
18	VDD_5V
19	ADJ
20	VSS
PIN ASSIGNMENT FOR RTP	
PIN NO.	SYMBOL
1	YU
2	YD
3	XR
4	XL

- NOTES:
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE.
  2. OPERATING VOLTAGE: VDD=3.3V.
  3. VIEWING DIRECTION: 12 O'CLOCK.
  4. IC DRIVER: HX8282-A01+HX8696-A01.
  5. OPERATING TEMP: -20°C ~ 70°C.
  6. STORAGE TEMP: -30°C ~ 80°C.
  7. SURFACE LUMINANCE: 800CD/M<sup>2</sup>(TYP.).
  8. LED BACKLIGHT: 36-CHIP WHITE LED, I<sub>f</sub>=240mA, V<sub>f</sub>=16.8V~20.4V.
  9. SURFACE TREATMENT: ANTI-GLARE.
  10. GENERAL TOLERANCE: ±0.3MM.
  11. ROHS COMPLIANT.


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DRAWN BY:	RITA	2019.07.29		MODULE P/N:
CHECKED BY:			SCALE : 1/1	M101021T-5RP
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/2	TFT + RTP MODULE

01	FIRST ISSUE	RITA	2019.07.29
VER.	REVISED DESCRIPTION	REVISER	DATE



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DRAWN BY:	RITA	2019.07.29		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI1010Z1T-5RP
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 2/2	TFT + RTP MODULE

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VER.	REVISED DESCRIPTION	REVISER	DATE