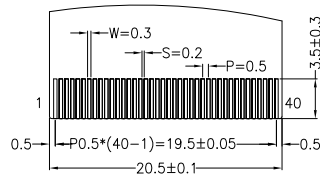
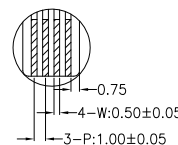


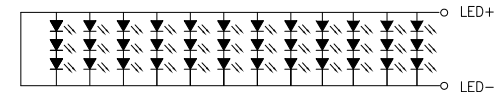
DETAIL A
(SCALE 50:1)



DETAIL B
(SCALE 4:1)



DETAIL C
(SCALE 4:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT FOR LCM			
NO.	SYMBOL	NO.	SYMBOL
1	VCOM	21	RXIN3+
2	VDD	22	GND
3	VDD	23	NC
4	NC	24	NC
5	RESET	25	GND
6	STBYB	26	NC
7	GND	27	DIMO
8	RXIN0-	28	SELB
9	RXIN0+	29	AVDD
10	GND	30	GND
11	RXIN1-	31	LED-
12	RXIN1+	32	LED-
13	GND	33	L/R
14	RXIN2-	34	U/D
15	RXIN2+	35	VGL
16	GND	36	CABCEN1
17	RXCLKIN-	37	CABCEN0
18	RXCLKIN+	38	VGH
19	GND	39	LED+
20	RXIN3-	40	LED+

PIN ASSIGNMENT FOR RTP			
NO.	SYMBOL	NO.	SYMBOL
1	XL	3	XR
2	YD	4	YU

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE.
2. OPERATING VOLTAGE: VDD=3.3V.
3. VIEWING DIRECTION: 12 O'CLOCK.
4. IC DRIVER: HX8696 + HX8282.
5. OPERATING TEMP: -20°C ~ 60°C.
6. STORAGE TEMP: -30°C ~ 70°C.
7. LED BACKLIGHT: 36-CHIP WHITE LED, If=240mA, Vf=9.9V±0.6V.
8. SURFACE LUMINANCE: 390CD/M²(TYP.).
9. TOP POLARIZER SURFACE TREATMENT: GLARE.
10. GENERAL TOLERANCE: ±0.3mm.
11. ROHS COMPLIANT.

MULTI-INNO TECHNOLOGY CO., LTD.			
DRAWN BY:	CAROL	20190115	MODULE P/N:
CHECKED BY:			MIO800RT-3RP
APPROVED BY:			DESCRIPTION:
VER.	REVISED DESCRIPTION	REVISER	DATE
02	CORRECT THE TOP POLARIZER SURFACE TREATMENT FROM ANTI-GLARE TO GLARE.	RITA	2021.01.25
01	FIRST ISSUE	CAROL	2014.03.14
VERSION NO:	02	SHEET NO.:	1/1