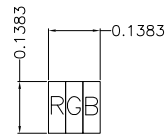
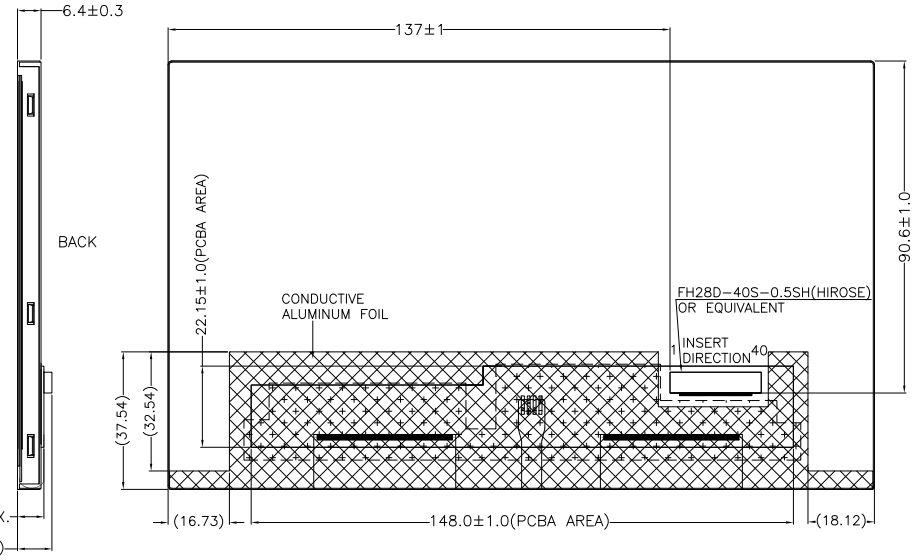
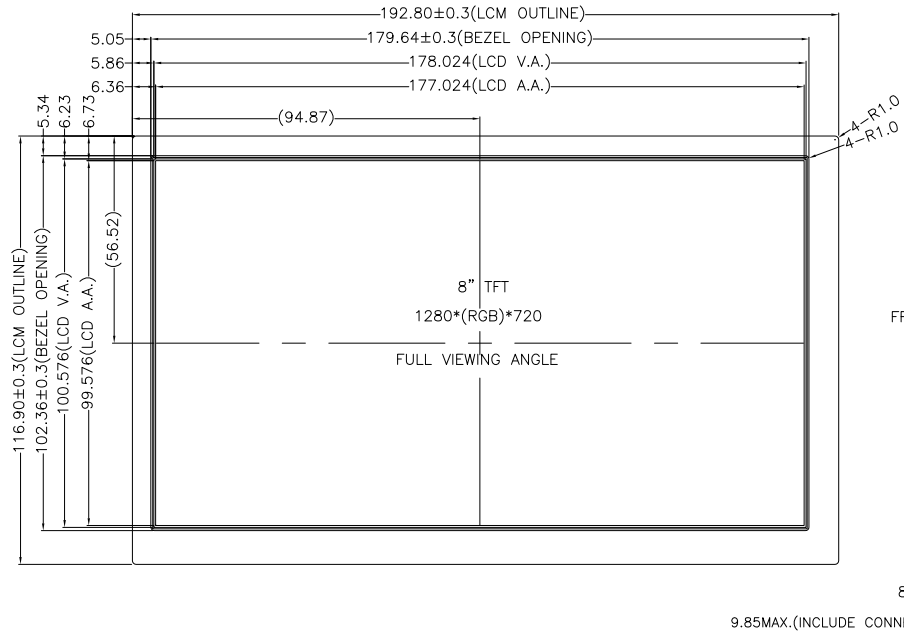
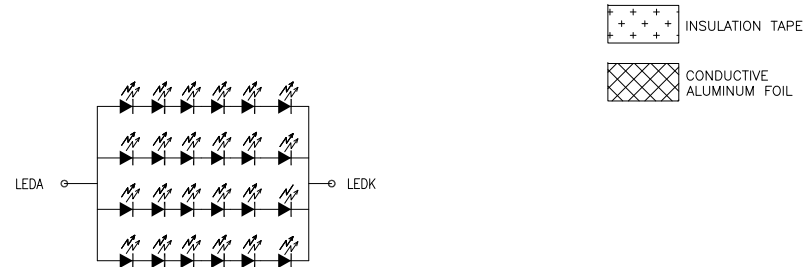


NO MECHANICAL HOUSING TO HOLD THE TFT AND PCBA ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE PCBA.

PIN ASSIGNMENT			
PIN	SYMBOL	PIN	SYMBOL
1	MODE	21	RXIN3+
2	VDD	22	GND
3	VDD	23	SCL
4	BISTEN	24	SDA
5	RESET	25	GND
6	STBYB	26	CSB
7	GND	27	NTC GND
8	RXINO-	28	SELB(DINT)
9	RXINO+	29	FCS
10	GND	30	GND
11	RXIN1-	31	LED-
12	RXIN1+	32	LED-
13	GND	33	L/R
14	RXCLKIN-	34	U/D
15	RXCLKIN+	35	NTC
16	GND	36	ATREN
17	RXIN2-	37	OTP_VDD
18	RXIN2+	38	NC
19	GND	39	LED+
20	RXIN3-	40	LED+



PIXEL PITCH



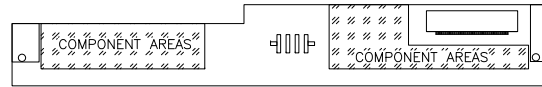
BACKLIGHT CIRCUIT DIAGRAM

NOTES:

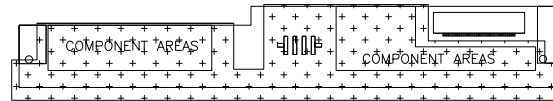
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: HX8249-A-LT + HX8695-E-LT
5. OPERATING TEMP.: -30°C ~ 85°C
6. STORAGE TEMP.: -40°C ~ 90°C
7. LED BACKLIGHT: 24-CHIP WHITE LED, IF=320MA, VF=16.8~20.4V
LIFE TIME: 50,000 HOURS (MIN.)
8. SURFACE LUMINANCE: 1000CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
11. GENERAL TOLERANCE: ±0.3MM
12. ROHS COMPLIANT

MULTI-INNO TECHNOLOGY CO.,LTD.			
DRAWN BY:	RITA	20220804	MODULE P/N:
CHECKED BY:			MI0800BA1T-1
APPROVED BY:			DESCRIPTION:
VERSION NO:	01		TFT MODULE
		SCALE : 1/1	UNIT: mm
		SHEET NO.: 1/2	

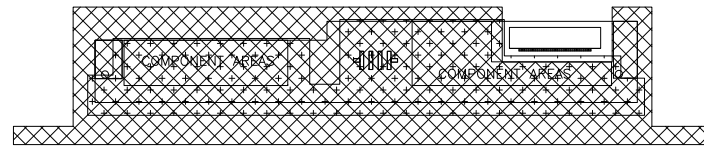
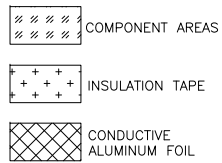
VER.	FIRST ISSUE	RITA	2022.08.04
VER.	REVISED DESCRIPTION	REVISER	DATE
01	FIRST ISSUE	RITA	2022.08.04



PCBA




PCBA WITH INSULATION TAPE



PCBA WITH INSULATION TAPE
WITH CONDUCTIVE ALUMINUM FOIL



MULTI-INNO TECHNOLOGY CO.,LTD.

DRAWN BY:	RITA	20220804		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0800BA1T-1
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 2/2	TFT MODULE

VER.	REVISED DESCRIPTION	REVISER	DATE
01	FIRST ISSUE	RITA	2022.08.04