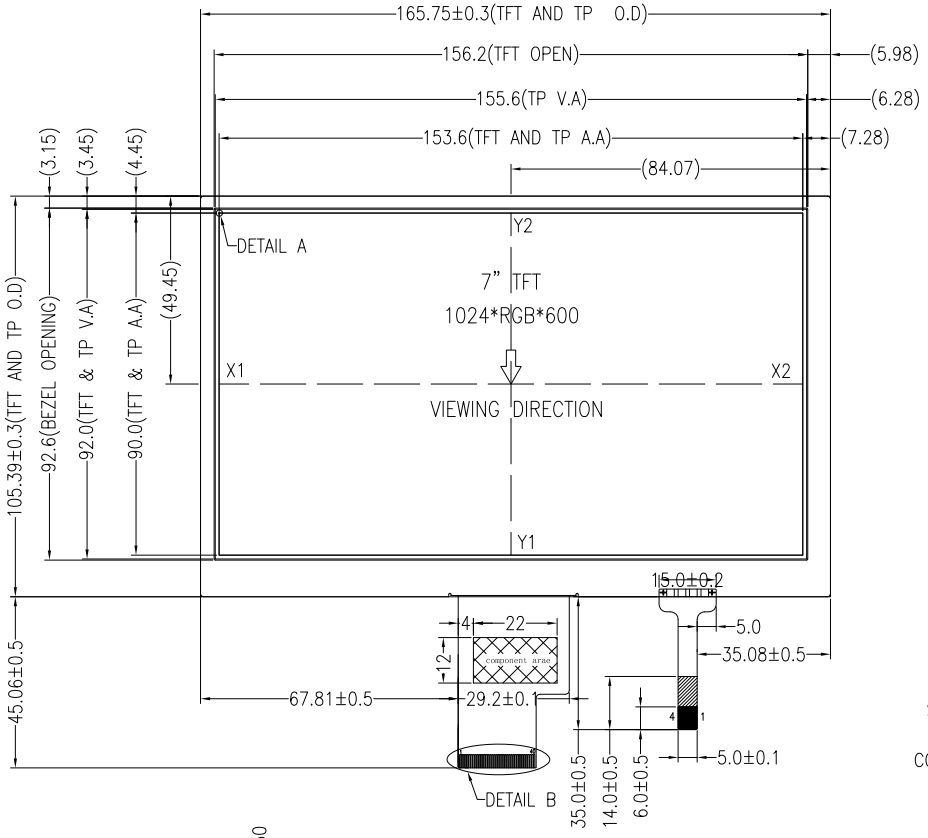
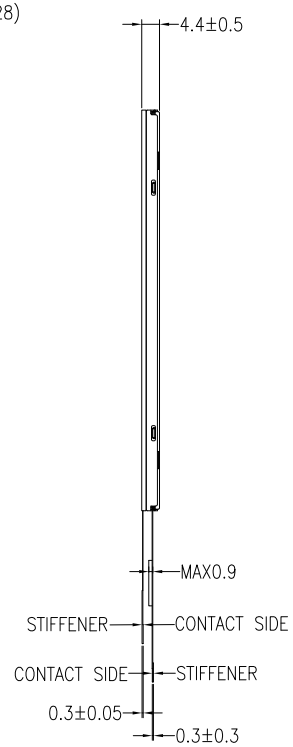
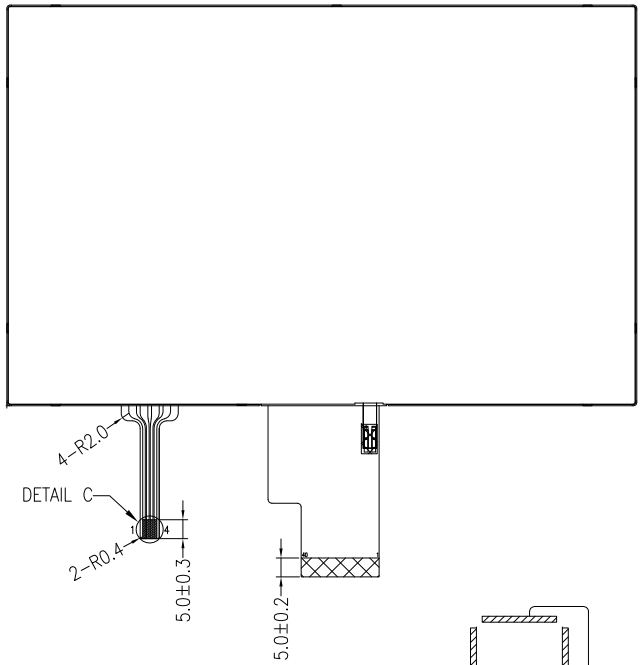
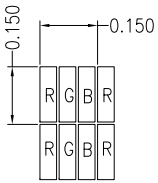




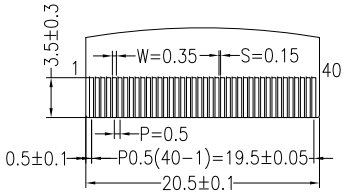


PIN DESCRIPTION FOR TFT	
PIN	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	DIMO
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	L/R
34	U/D
35	VGL
36	CABCEN1
37	CABCEN0
38	VGH
39	LED+
40	LED+

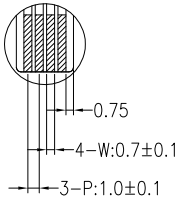
PIN DESCRIPTION FOR RTP	
PIN	SYMBOL
1	Y2
2	X2
3	Y1
4	X1



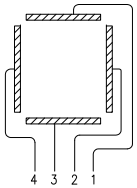
DETAIL A
(SCALE 30:1)



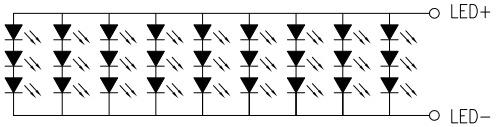
DETAIL B
(SCALE 3:1)



DETAIL C
(SCALE 3:1)



TP BLOCK DIAGRAM



BACKLIGHT CIRCUIT DIAGRAM

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: 12 O'CLOCK
4. DRIVER IC: HX8282+HX8677
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: 27-CHIP WHITE LED, IF=180mA, VF=9.6V
8. SURFACE LUMINANCE: 400CD/M^2 (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: GLARE
10. THE SINGLE LAYER AREA OF FPC IS BENDABLE AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5MM
11. GENERAL TOLERANCE: ±0.3MM
12. ROHS COMPLIANT

VER.	REVISED DESCRIPTION	REVISER	DATE
02	MODIFY THE DRIVER IC	CAROL	2016.02.24
01	FIRST ISSUE	RITA	2016.07.13

MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA 2016.07.13		MODULE P/N: MI0700XT-19RP
CHECKED BY:		SCALE : 1/1	
APPROVED BY:		UNIT: mm	DESCRIPTION: TFT + RTP MODULE
VERSION NO:	02	SHEET NO.: 1/1	