



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT

PIN NO	SYMBOL
1	U/D
2	NC
3	HSYNC
4	VLED
5	VLED
6	VLED
7	VCC
8	VSYNC
9	DE
10	X2
11	Y1
12	ADJ
13	B5
14	B4
15	B3
16	VSS
17	B2
18	B1
19	B0
20	VSS
21	G5
22	G4
23	G3
24	VSS
25	G2
26	G1
27	G0
28	VSS
29	R5
30	R4
31	R3
32	VSS
33	R2
34	R1
35	R0
36	X1
37	Y2
38	DCLK
39	VSS
40	L/R

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE.
2. OPERATING VOLTAGE: VCC=3.3V, VLED=5.0V.
3. VIEWING DIRECTION: 6 O'CLOCK.
4. IC DRIVER: HX8250-A01+HX8678A.
5. OPERATING TEMP.: -20°C ~ 70°C.
6. STORAGE TEMP.: -30°C ~ 80°C.
7. SURFACE LUMINANCE: 780CD/M² (TYP.).
8. LED BACKLIGHT: 21-CHIP WHITE LED, If=155mA, Vf=9.2V.
9. TOP POLARIZER SURFACE TREATMENT: GLARE.
10. RECOMMENDED CUSHION ADHERENT AREA: TP V.A+1.6mm.
11. GENERAL TOLERANCE: ±0.2mm.
12. ROHS COMPLIANT.

		MULTI-INNO TECHNOLOGY CO.,LTD.	
DRAWN BY:	RITA	2018.06.22	
CHECKED BY:			SCALE : 1/1
APPROVED BY:			UNIT: mm
VERSION NO:	02		SHEET NO.: 1/1
MODULE P/N:			M10570E2T-2RP
DESCRIPTION:			TFT & RTP MODULE

VER.	REVISED DESCRIPTION	REVISER	DATE
02	OPTIMIZE PCB'S DESIGN.	CAROL	2020.02.28
01	FIRST ISSUE	RITA	2018.06.22