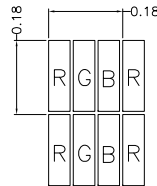
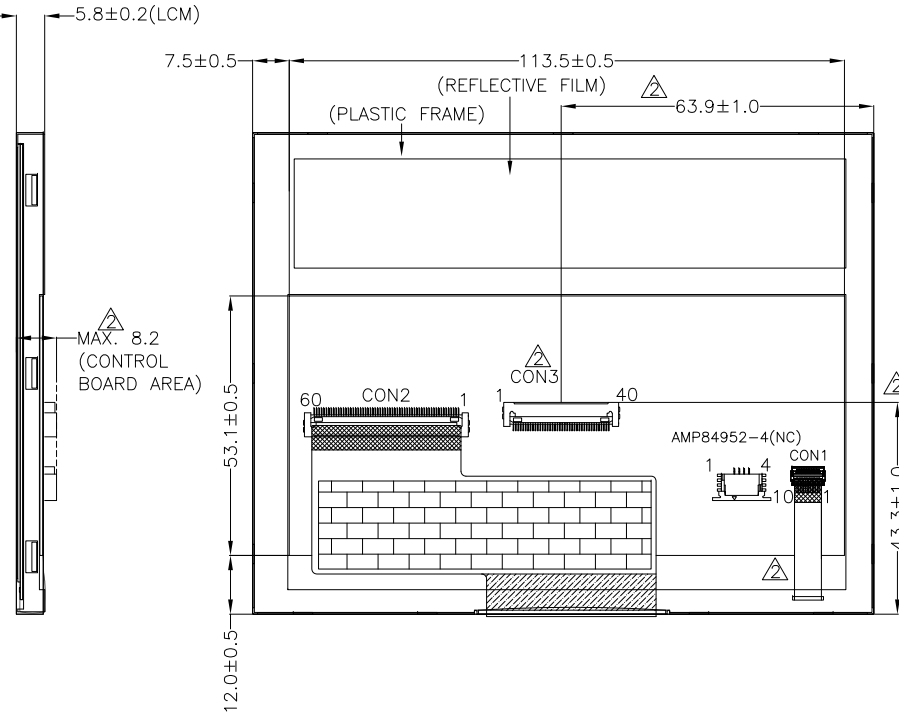
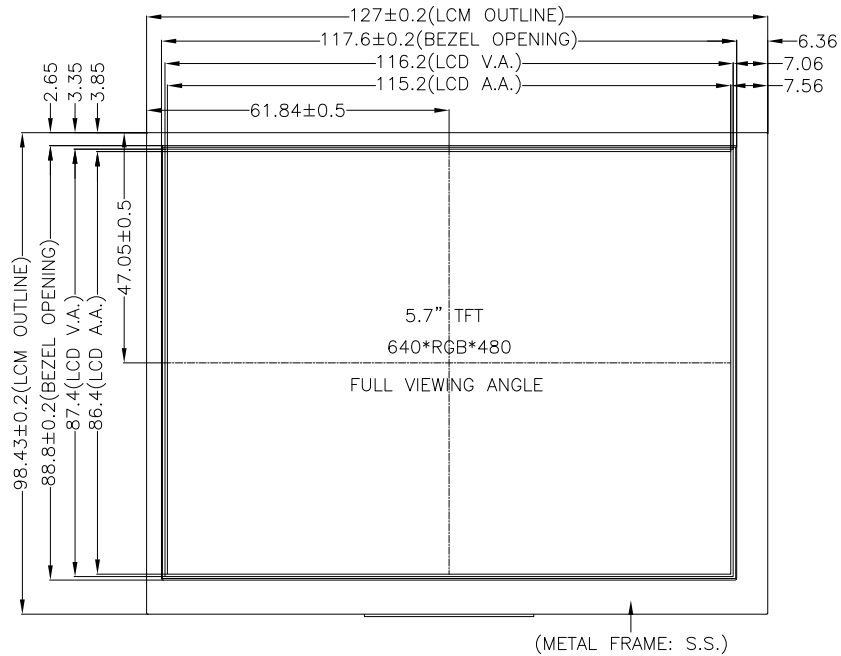


NO MECHANICAL HOUSING TO HOLD THE TFT AND PCBA PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE PCBA.

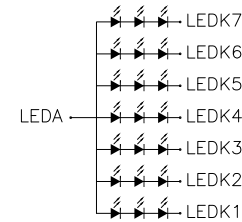


PIN ASSIGNMENT FOR CON3

PIN	SYMBOL	PIN	SYMBOL
1	U/D	21	G5
2	NC	22	G4
3	HSYNC	23	G3
4	VLED	24	VSS
5	VLED	25	G2
6	VLED	26	G1
7	VCC	27	G0
8	VSYNC	28	VSS
9	DE	29	R5
10	NC (XL)	30	R4
11	NC (YU)	31	R3
12	ADJ	32	VSS
13	B5	33	R2
14	B4	34	R1
15	B3	35	R0
16	VSS	36	NC (XR)
17	B2	37	NC (YD)
18	B1	38	DCLK
19	B0	39	VSS
20	VSS	40	L/R



PIXEL DETAIL




BACKLIGHT CIRCUIT DIAGRAM

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: JD9168S
5. OPERATING TEMP.: -30°C ~ 80°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: 21-CHIP WHITE LED
BUILT-IN BACKLIGHT DRIVER: VLED=5.0V (TYP.)
8. SURFACE LUMINANCE: 700CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
11. GENERAL TOLERANCE: ±0.3MM
12. ROHS COMPLIANT

 MULTI-INNO TECHNOLOGY CO., LTD.

02	MODIFY PCBA DESIGN, I/O DEFINITIONS AND BACKLIGHT FPC DESIGN. MODIFY THE MODULE THICKNESS OF CONTROL BOARD AREA.	RITA	2022.08.31	CHECKED BY:	RITA	2022.07.28		MODULE P/N:	MI0570AET-2
01	FIRST ISSUE	RITA	2022.07.28	APPROVED BY:			SCALE : 1/1	DESCRIPTION:	TFT MODULE
VER.	REVISED DESCRIPTION	REVISOR	DATE	VERSION NO:	02		UNIT: mm		
							SHEET NO.: 1/1		