

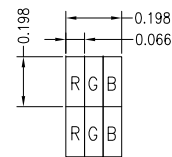
PIN DESCRIPTION FOR TFT	
PIN	SYMBOL
1	LED-
2	LED+
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	PCLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC
36	GND
37	NC
38	NC
39	NC
40	NC
PIN DESCRIPTION FOR CTP	
PIN	SYMBOL
1	VSS
2	VCC
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: 12 O'CLOCK
4. DRIVER IC: HX8257A
5. LED BACKLIGHT: 10-CHIP WHITE LED, If=40mA, Vf=16V±1.0V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

NOTES FOR CTP:

1. 4.3 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VCC=3.3V
4. DRIVER IC: FT5446-P03 (SMT)
5. RESOLUTION: 480*272
6. MULTI FINGER: UP TO 5
7. HARDNESS OF COVER SURFACE: 7H
8. FIRMWARE FILE: TBD



DETAIL A (SCALE 35:1)



BACKLIGHT CIRCUIT DIAGRAM

NOTES FOR OTHERS:

1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 460 CD/M² (TYP.)
4. THE SINGLE LAYER AREA OF FPC IS BENDABLE AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5MM
5. GENERAL TOLERANCE: ±0.2MM
6. ROHS COMPLIANT

MULTI-INNO TECHNOLOGY CO., LTD.			
DRAWN BY:	RITA	2017.04.27	MODULE P/N:
CHECKED BY:			MIO430H5T-2CP3
APPROVED BY:			DESCRIPTION:
VERSION NO:	02		TFT + CTP MODULE
UNIT:	mm	SCALE :	1/1
SHEET NO.:	1/1		

VER.	REVISED DESCRIPTION	REVISOR	DATE
02	UPDATE THE IC DRIVER OF CTP FROM FT5446DQS TO FT5446-P03.	RITA	2019.04.03
01	FIRST ISSUE	RITA	2017.04.27