



PIN ASSIGNMENT	
PIN	SYMBOL
1	VLED_K
2	VLED_A
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	DCLK
31	DISP
32	HSYN
33	VSYN
34	DE
35	NC
36	GND
37	NC(XR)
38	NC(YD)
39	NC(XL)
40	NC(YU)

- NOTES:
1. DISPLAY TYPE: TFT/TRANSMISSIVE/NORMALLY BLACK.
 2. VIEWING DIRECTION: FULL VIEWING ANGLE.
 3. OPERATING VOLTAGE: VDD=3.3V.
 4. DRIVER IC: HX8264D03+HX8664B.
 5. OPERATING TEMP.: -30°C ~ 85°C.
 6. STORAGE TEMP.: -40°C ~ 85°C.
 7. BACKLIGHT: 10-CHIP WHITE LED, IF=40mA, Vf=14.0V~17.0V.
 8. SURFACE LUMINANCE: 500CD/M²(TYP.).
 9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE.
 10. GENERAL TOLERANCE: ±0.2mm.
 11. ROHS COMPLIANT.

DETAIL A
(SCALE 4:1)

BACKLIGHT CIRCUIT DIAGRAM

MULTI-INNO TECHNOLOGY CO.,LTD.			
DRAWN BY:	RITA	2020.07.30	MODULE P/N:
CHECKED BY:			MI0430BFT-1
APPROVED BY:			DESCRIPTION:
VER.	01	FIRST ISSUE	TFT MODULE
REVISER	RITA	DATE	VERSION NO.
			SCALE : 1/1
			UNIT: mm
			SHEET NO.: 1/1

01	FIRST ISSUE	RITA	2020.07.30
VER.	REVISED DESCRIPTION	REVISER	DATE