



NOTES FOR TFT:

1. DISPLAY TYPE: TFT / TRANSMISSIVE/NORMALLY BLACK
2. OPERATING VOLTAGE: VCI=3.3V
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: HX8394F
5. BACKLIGHT: 8-CHIP WHITE LED, IF=40mA, VF=12.8V±0.8V

NOTES FOR CTP:

1. 4.0INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: TP_VCI=2.8V~3.3V
4. DRIVER IC: FT5446-P03
5. FIRWARE FILE: MI0400KT-1CP2_P-FT5446P03_V02_D02_20200704_all.bin

OTHERS:

1. OPERATING TEMP.: -20°C ~70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 300CD/M²(TYP.)
4. THE SINGLE LAYER AREA OF FPC IS BENDABLE AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5mm
5. GENERAL TOLERANCE: ±0.2mm
6. ROHS COMPLIANT

PIN	ASSIGNMENT
1	LEDA
2	LEDK1
3	LEDK2
4	VCI
5	IOVCC
6	RESET
7	TE
8	PWM
9	GND
10	D0P
11	D0N
12	GND
13	D1P
14	D1N
15	GND
16	CLKP
17	CLKN
18	GND
19	D2P
20	D2N
21	GND
22	D3P
23	D3N
24	GND
25	TP_INT
26	TP_SDA
27	TP_SCL
28	TP_RESET
29	TP_VCI
30	TP_IOVCC

MI MULTI-INNO TECHNOLOGY CO.,LTD.

DRAWN BY:	RITA	2020.04.01	SCALE : 1/1	MODULE P/N:
CHECKED BY:			UNIT: mm	MI0400KT-1CP2
APPROVED BY:				DESCRIPTION:
VERSION NO:	04			TFT & CTP MODULE

VER.	REVISED DESCRIPTION	REVISER	DATE
04	CORRECT COVER THICKNESS FROM 0.5MM TO 0.7MM.	CAROL	2020.09.17
03	ADD CIP FIRWARE FILE; OPTIMIZE TFT FPC; MODIFY CIP-FPC AND OCA BONDING.	CAROL	2020.07.06
02	CORRECT THE SINGLE LAYER AREA OF TFT FPC; CORRECT THE STRUCTURE OF CTP.	RITA	2020.04.24
01	FIRST ISSUE	RITA	2020.04.01