

TFT FPC I/O:		
1	LEDK	29 DB3
2	LEDA	30 DB2
3	LEDA	31 DB1
4	LEDA	32 DB0
5	LEDA	33 SDO
6	IMO	34 SDI
7	IM1	35 RD
8	IM2	36 WRX/SCL
9	NC	37 D/CX
10	RESET	38 CSX
11	VSYNC	39 TE
12	HSYNC	40 VDDI
13	DOTCLK	41 VDDI
14	DE	42 VCI
15	DB17	43 GND
16	DB16	44 NC
17	DB15	45 NC
18	DB14	46 NC
19	DB13	47 NC
20	DB12	48 GND
21	DB11	49 GND
22	DB10	50 GND
23	DB9	
24	DB8	
25	DB7	
26	DB6	
27	DB5	
28	DB4	

CTP FPC I/O:	
1	VSS
2	VCC
3	SCL
4	NC
5	SDA
6	NC
7	RST
8	NC
9	INT
10	VSS

NOTES FOR TFT:

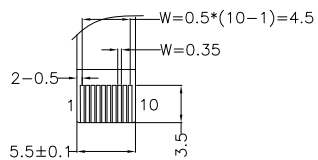
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDDI/VCI=2.8V.
3. VIEWING DIRECTION: 6 O'CLOCK
4. DRIVER IC: ILI9488.
5. LED BACKLIGHT: 6-CHIP WHITE LED.IF=120mA, VF=2.7V ~ 3.4V.
6. TOP POLARIZER SURFACE TREATMENT: GLARE.

NOTES FOR CTP:

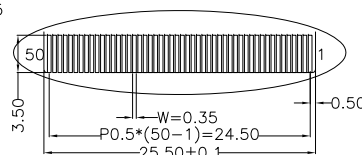
1. 3.5 INCH PROJECTIVE CAPACITIVE TOUCH PANEL.
2. COVER GLASS + SENSOR GLASS + FPCA.
3. OPERATING VOLTAGE: VCC=2.8V~3.3V.
4. RESOLUTION: 320 X 480 DOTS.
5. IC DRIVER: FT5446-P03, INTERFACE: IIC.
6. SURFACE HARDNESS: >=6H(750g).
7. TOUCH NUMBERS: 5.

OTHERS:

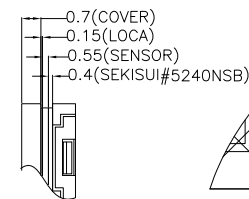
1. GENERAL TOLERANCE: ±0.2MM.
2. OPERATING TEMP.: -20°~70°.
3. STORAGE TEMP.: -30°~80°.
4. SURFACE LUMINANCE: 320CD/M<sup>2</sup>(TYP.).
5. ROHS COMPLIANT.
6. THERE ARE ±0.2MM TOLERANCE WHILE CTP ASSEMBLES TO TFT.



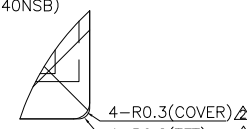
DETAIL A  
(SCALE 2:1)



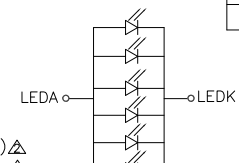
DETAIL B  
(SCALE 2:1)



DETAIL C  
(SCALE 2:1)



DETAIL D  
(SCALE 10:1)



BACKLIGHT CIRCUIT DIAGRAM

VER.	REVISED DESCRIPTION	REVISER	DATE
03	MODIFY THE DRIVER IC FROM FT5446DQS TO FT5446-P03 ADD DOUBT SIDE EMI COATING MODIFY THE CTP FPC SHAPE AND BOND POSITION CORRECT THE SIZE OF SENSOR OD TO TFT OD	JERRY	2020.03.12
02	ADD CHAMFER OF TFT(R-0.2) AND COVER LENS(R-0.3) CORRECT THE MODULE TOTAL THICKNESS	JERRY	2020.02.19
01	FIRST ISSUE	JERRY	2020.02.17



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	JERRY	2020.03.12		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0350EAT-7CP5
APPROVED BY:			UNIT: MM	DESCRIPTION:
VERSION NO:	03		SHEET NO.: 1/1	TFT + CTP MODULE