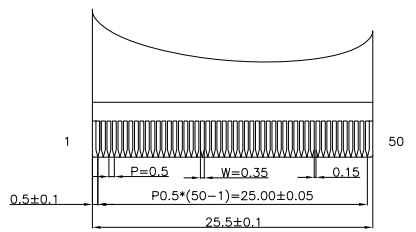
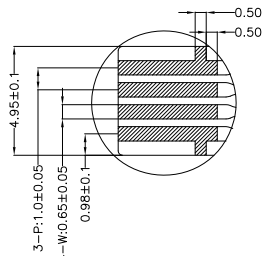


DETAIL A
(SCALE 40:1)



DETAIL B
(SCALE 2:1)



DETAIL C
(SCALE 4:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT FOR LCM	
PIN NO.	SYMBOL
1	VLEDA
2	GND
3	VLEDK
4	GND
5	VSHD
6	GND
7	GND
8	GND
9	VSYNC
10	GND
11	RESET
12	GND
13	GND
14	GND
15	CS
16	GND
17	SDO
18	SDI
19	GND
20	SCL
21	GND
22	B5
23	B4
24	B3
25	B2
26	B1
27	BO
28	ENAB
29	GND
30	HSYNC
31	GND
32	DCLK
33	GND
34	G5
35	G4
36	G3
37	G2
38	G1
39	G0
40	GND
41	R5
42	R4
43	R3
44	R2
45	R1
46	RO
47	GND
48	GND
49	GND
50	GND

PIN ASSIGNMENT FOR TP	
NO.	SYMBOL
1	YU
2	XR
3	YD
4	XL

- NOTES:
1. DISPLAY TYPE: TFT/TRANSFLECTIVE/NORMALLY WHITE.
 2. OPERATING VOLTAGE: VSHD=3.0V.
 3. VIEWING DIRECTION: 6:00 O'CLOCK.
 4. DRIVER IC: ILI9341V.
 5. OPERATING TEMP.: -20°~70°.
 6. STORAGE TEMP.: -30°~80°.
 7. BACKLIGHT: 6-CHIP WHITE LED, IF=20mA, VF=19.2V±1.2V.
 8. SURFACE LUMINANCE: 80CD/M²(TYP.).
 9. TOP POLARIZER SURFACE TREATMENT: GLARE.
 10. GENERAL TOLERANCE: ±0.2MM.
 11. ROHS COMPLIANT.

MIT MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2018.12.26		MODULE P/N:	
CHECKED BY:				SCALE :	1/1
APPROVED BY:				UNIT:	mm
VERSION:	02			SHEET NO.:	1/1
DESCRIPTION:	TFT + RTP MODULE				

VER.	REVISED DESCRIPTION	REVISER	DATE	VERSION NO:
02	CORRECT TOP POLARIZER SURFACE TREATMENT FROM ANTI-GLARE TO GLARE.	CAROL	2021.05.28	
01	FIRST ISSUE	RITA	2018.12.26	