



PIN DESCRIPTION FOR CTP		PIN DESCRIPTION FOR TFT	
PIN	SYMBOL	PIN	SYMBOL
1	VSS	1	VCI
2	VDD	2	VCI
3	SCL	3	VSS
4	NC	4	VDDIO
5	SDA	5	VSS
6	NC	6	RESET
7	/RST	7	DC/SDC(RS)
8	NC	8	E/RD
9	/INT	9	WR
10	VSS	10	CS
		11	SCL
		12	SDO
		13	SDI
		14	WSYNC(NC)
		15	D17
		16	D16
		17	D15
		18	D14
		19	D13
		20	D12
		21	D11
		22	D10
		23	D9
		24	D8
		25	D7
		26	D6
		27	D5
		28	D4
		29	D3
		30	D2
		31	D1
		32	D0
		33	VSS
		34	DOTCLK
		35	HSYNC
		36	VSYNC
		37	OE
		38	VSS
		39	PS0
		40	PS1
		41	PS2
		42	PS3
		43	VSS
		44	NC
		45	NC
		46	NC
		47	NC
		48	VSS
		49	LEDK
		50	LEDA

- NOTES FOR TFT:
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
 2. OPERATING VOLTAGE: VCI/VDDIO=3.3V (TYP.)
 3. VIEWING DIRECTION: 12 O'CLOCK
 4. DRIVER IC: SSD2119
 5. LED BACKLIGHT: 6-CHIP WHITE LED, If=20mA, Vf=19.2V
 6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
 7. RECOMMENDED CASE OPEN AREA SHOULD BE LESS THAN MODULE V.A

- NOTES FOR CTP:
1. 3.5 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
 2. COVER GLASS + SENSOR GLASS + FPCA
 3. OPERATING VOLTAGE: VCC=3.3V
 4. DRIVER IC: FT5448 (SMT), I2C INTERFACE
 5. RESOLUTION: 320*240
 6. MULTI FINGER: UP TO 5
 7. HARDNESS OF COVER SURFACE: 7H

- NOTES FOR OTHERS:
1. OPERATING TEMP.: -20°C ~ 70°C
 2. STORAGE TEMP.: -30°C ~ 80°C
 3. SURFACE LUMINANCE: 380 CD/M² (TYP.)
 4. GENERAL TOLERANCE: ±0.2MM
 5. ROHS COMPLIANT

MIT MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY: VICTOR 2016.09.30 MODULE P/N: MIO350ADT-5CP3

CHECKED BY: SCALE : 1/1

APPROVED BY: UNIT: mm DESCRIPTION: TFT + CTP MODULE

VERSION NO: 02 SHEET NO.: 1/1

VER.	REVISED DESCRIPTION	REVISER	DATE
02	OPTIMIZE SENSOR STRUCTURE DESIGN	RITA	2017.09.01
01	FIRST ISSUE	VICTOR	2016.09.30