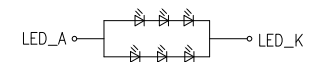


DETAIL A
(SCALE 50:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT			
1	GND	31	G02
2	NC(YU)	32	G03
3	NC(XR)	33	G04
4	NC(YD)	34	G05
5	NC(XL)	35	GND
6	GND	36	R00
7	NC	37	R01
8	NC	38	R02
9	GND	39	R03
10	NC	40	R04
11	NC	41	R05
12	NC	42	GND
13	NC	43	VDD
14	NC	44	NC
15	GND	45	GND
16	NC	46	DCLK
17	RESET	47	GND
18	NC	48	ENABLE
19	NC	49	GND
20	VCI	50	CS
21	GND	51	SDA
22	B00	52	NC
23	B01	53	SCL
24	B02	54	VSYNC
25	B03	55	HSYNC
26	B04	56	NC
27	B05	57	NC
28	GND	58	LED-
29	G00	59	LED+
30	G01	60	GND

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: 9 O'CLOCK
4. DRIVER IC: HX8363-A03
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: 6-CHIP WHITE LED, IF=40mA, VF=17.7V~19.2V
8. SURFACE LUMINANCE: 1000CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. THE SINGLE LAYER AREA OF FPC IS BENDABLE AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5MM
11. GENERAL TOLERANCE: ±0.2MM
12. ROHS COMPLIANT

MULTI-INNO TECHNOLOGY CO., LTD.			
DRAWN BY:	RITA	2018.10.30	MODULE P/N:
CHECKED BY:			MI0350ACT-9
APPROVED BY:			SCALE : 1/1
VERSION NO:	01		UNIT: mm
			SHEET NO.: 1/1
			DESCRIPTION: TFT MODULE

01	FIRST ISSUE	RITA	2018.10.30
VER.	REVISED DESCRIPTION	REVISOR	DATE