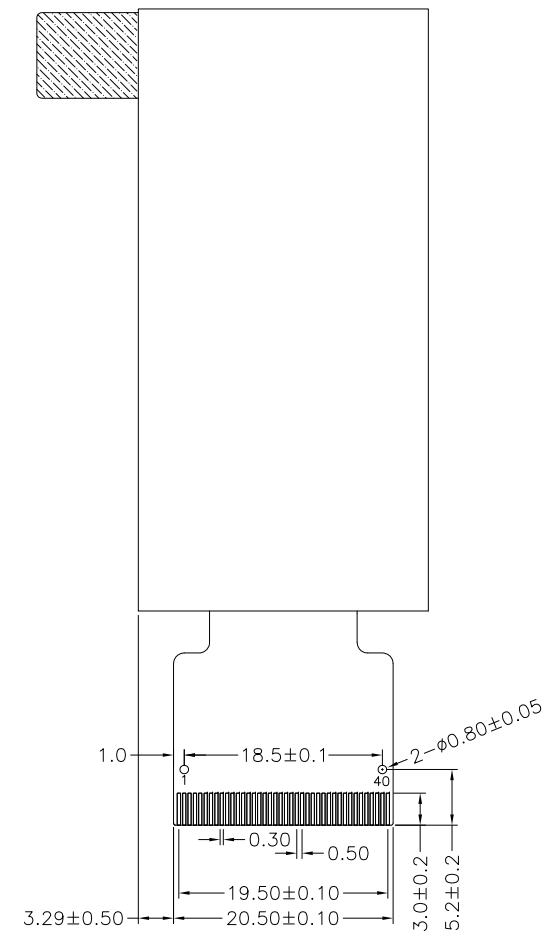
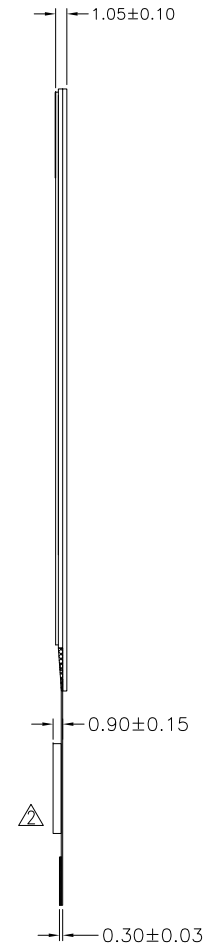
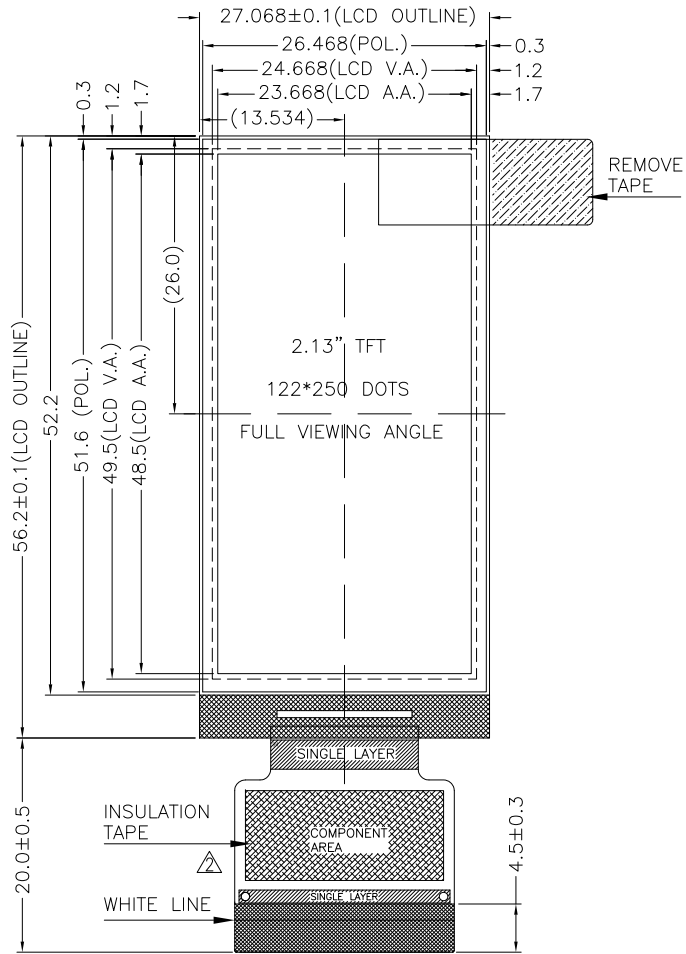


NO MECHANICAL HOUSING TO HOLD THE TFT AND THE BACKLIGHT UNIT ON THIS PRODUCT, SO THE END USER'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE BACKLIGHT UNIT.




PIN ASSIGNMENT	
NO	SYMBOL
1	NC
2	GND
3	NC
4	NC
5	NC
6	NC
7	NC
8	NC
9	NC
10	NC
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	NC
18	NC
19	NC
20	NC
21	NC
22	NC
23	NC
24	NC
25	NC
26	NC
27	NC
28	VDDA
29	VDDI
30	GND
31	CS
32	A0
33	RST
34	SCL
35	SDA
36	NC
37	NC
38	GND
39	NC
40	NC

NOTES:

1. DISPLAY TYPE: MONO TFT, REFLECTIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDDI=2.8V
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. IC DRIVER: ST7305
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. TOP POLARIZER SURFACE TREATMENT: GLARE
8. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
9. GENERAL TOLERANCE: ±0.2MM
10. ROHS COMPLIANT

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02	MODIFY THE IC DRIVER FROM ST7302 TO ST7305. ADD COMPONENT AREA ON THE FPC. MODIFY THE I/O DEFINITIONS OF PIN3~PIN27, PIN36~PIN37.	RITA	2023.04.20	DRAWN BY: RITA 2020.09.04		MODULE P/N: MI0213AT-2
01	FIRST ISSUE	RITA	2020.09.04	CHECKED BY:	SCALE : 1/1	DESCRIPTION: TFT MODULE
VER.	REVISED DESCRIPTION	REVISER	DATE	APPROVED BY:	UNIT: mm	
				VERSION NO: 02	SHEET NO.: 1/1	