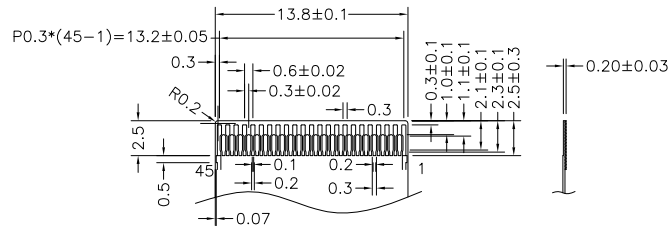
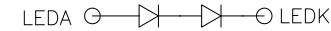


PIXEL DETAIL
(SCALE 50:1)



DETAIL A
(SCALE 2:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT	
PIN	SYMBOL
1	LEDA
2	NC
3	LEDK
4	NC
5	GND
6	GND
7	VCC
8	VCC
9	IOVCC
10	SDO
11	DB17
12	DB16
13	DB15
14	DB14
15	DB13
16	DB12
17	DB11
18	DB10
19	DB9
20	DB8
21	DB7
22	DB6
23	DB5
24	DB4
25	DB3
26	DB2
27	DB1
28	DB0
29	SDA
30	PCLK
31	DE
32	HSYNC
33	VSYNC
34	RD
35	WR(RS)
36	RS(SCL)
37	CSX
38	RESET
39	IM0
40	IM1
41	IM2
42	NC
43	NC
44	NC
45	NC

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=2.8V(TYP.), IOVCC=1.8V/2.8V(TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: GC9A01A
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: 2-CHIP WHITE LED, If=20mA, Vf=5.6V~6.8V
8. SURFACE LUMINANCE: 450CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: GLARE
10. RECOMMEND MATCHING CONNECTOR: FH26-45S-0.3SHW OR EQUIVALENT
11. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
12. GENERAL TOLERANCE: ±0.2MM
13. ROHS COMPLIANT

IM3	IM2	IM1	IM0	INTERFACE	READ BACK DATA BUS SELECTION
1	1	0	1	3-LINE 9BIT SERIAL I/F	SDA: IN/OUT
				2 DATA LANE SERIAL I/F	SDA: IN/OUT, DCX: IN
1	1	1	1	4-LINE 8BIT SERIAL I/F	SDA: IN/OUT
1	0	0	1	3-LINE 9BIT SERIAL I/F II	SDA: IN/SDO: OUT
1	0	1	1	4-LINE 8BIT SERIAL I/F II	SDA: IN/SDO: OUT

1. IF NOT USE PIN.FIX TO THE GND,IOVCC OR NC
2. IM3 IS CONNECTED TO IOVCC ON FPC

Mit MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2022.02.25		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0128BT-2
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT MODULE

01	FIRST ISSUE	RITA	2022.02.25
VER.	REVISED DESCRIPTION	REVISER	DATE