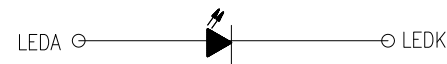


DETAIL A  
(SCALE 40:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN DESCRIPTION	
PIN	SYMBOL
1	NC
2	NC
3	NC
4	NC
5	NC
6	NC
7	NC
8	GND
9	VDDI
10	NC
11	SPI
12	IM2
13	CSX
14	RESX
15	RS/SCL
16	WRX/AO
17	RDX
18	DO/SDA
19	D1
20	D2
21	D3
22	D4
23	D5
24	D6
25	D7
26	NC
27	NC
28	VDD
29	LEDA
30	LEDK

IM2	SPI	INTERFACE	PIN IN USE
1	0	MCU 8-BIT	CSX,RESX,RS,WRX,RDX,D0-D7
0	0	3-LINE SPI	CSX,RESX,SCL,SDA
	1	4-LINE SPI	CSX,RESX,A0,SCL,SDA

NOTE: RDX, A0, D1-D7, IF NOT USED, PLEASE TIE IT TO GND.

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDD=2.8V(TYP.), VDDI=1.8V/2.8V
3. VIEWING DIRECTION: 6 O'CLOCK
4. DRIVER IC: ST7735S
5. INTERFACE: 3-/4-WIRE SPI, MCU 8-BIT PARALLEL
6. OPERATING TEMP.: -20°C ~ 70°C
7. STORAGE TEMP.: -30°C ~ 80°C
8. LED BACKLIGHT: 1-CHIP WHITE LED, IF=15mA, VF=2.8V~3.2V
9. SURFACE LUMINANCE: 240CD/M<sup>2</sup> (TYP.)
10. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
11. RECOMMENDED CASE OPEN AREA SHOULD BE LESS THAN MODULE V.A
12. GENERAL TOLERANCE: ±0.2MM
13. ROHS COMPLIANT

<b>MULTI-INNO TECHNOLOGY CO., LTD.</b>		DRAWN BY: JACK	2017.09.18	SCALE : 1/1	MODULE P/N:
		CHECKED BY:			UNIT: mm
VER. REVISED DESCRIPTION		REVISER	DATE	VERSION NO:	DESCRIPTION:
02	CORRECT THE OPERATING AND STORAGE TEMPERATURE	RITA	2017.12.06	02	TFT MODULE
01	FIRST ISSUE	JACK	2017.09.18		
				SHEET NO.: 1/1	